

Junior ASIC Design Engineer

Job Offer

The Company

3Brain AG is a Swiss deep tech company working on cell-electronic interfaces that link biological networks to computers via custom-made semiconductor microchips. Our mission is to empower life scientists to probe intelligent networks, accelerate biomedical discoveries for the benefit of patients, and create a future more exciting than the present. Our interdisciplinary team of scientists and engineers are constantly pushing the boundaries of engineering for life's complexities. We are ambitious, fast-growing and love to explore. The company is headquartered in Switzerland nearby Zurich, has a R&D office in Genova, Italy, and has installed its cutting-edge products in laboratories and pharma companies worldwide.

More info: <http://www.3brain.com/about.html>.

Job description

For the next expansion step, 3Brain is looking for a Junior ASIC Design Engineer to reinforce our R&D team. The candidate will be part of a group of ASIC design engineers and help plan and design the future CMOS-sensor products of 3Brain.

Main responsibilities:

- Design and implementation (schematics, simulation, layout, post-layout verification) of next-generation CMOS-based biological sensor systems.
- Document design methods, specifications, functional requirements and design choices.
- Create detailed test specifications and reports.
- Maintain an up-to-date knowledge of relevant literature and state-of-the-art circuit designs.

Required Qualifications

- MSc/PhD in Microelectronics (analog/mixed-signal ASIC-design).
- Experience in a modern CMOS analog electronics workflow (schematics, layout, verification) and a good understanding of the digital toolchain.



- Understanding of various analog building blocks, such as ADCs, DACs, LNAs, filters, biasing, etc.
- Fluency in English.
- Great team spirit, passion, outstanding personality and perseverance.
- Initiative, independence and ability to communicate and work effectively with other team members in a multicultural and international environment.
- Applicant must already have a work permit for the Schengen area.

Additional Qualifications

- Fluency in German.
- Knowledge of Python.

What We Offer

- A dynamic and stimulating work environment driven by a desire to build transformational technologies to help others.
- A young, fast-growing company with plenty of opportunities for personal growth and development.
- Great challenges and adventures in both technical and economical fields.
- Working place: **Pfäffikon SZ, Switzerland**. It is a beautiful lakeside village just outside of Zurich, the country's main cultural, financial and business center.

To apply, send your CV and short motivation letter to hr@3brain.com, subject: [Junior ASIC Design Engineer](#).

